

FIG. 1

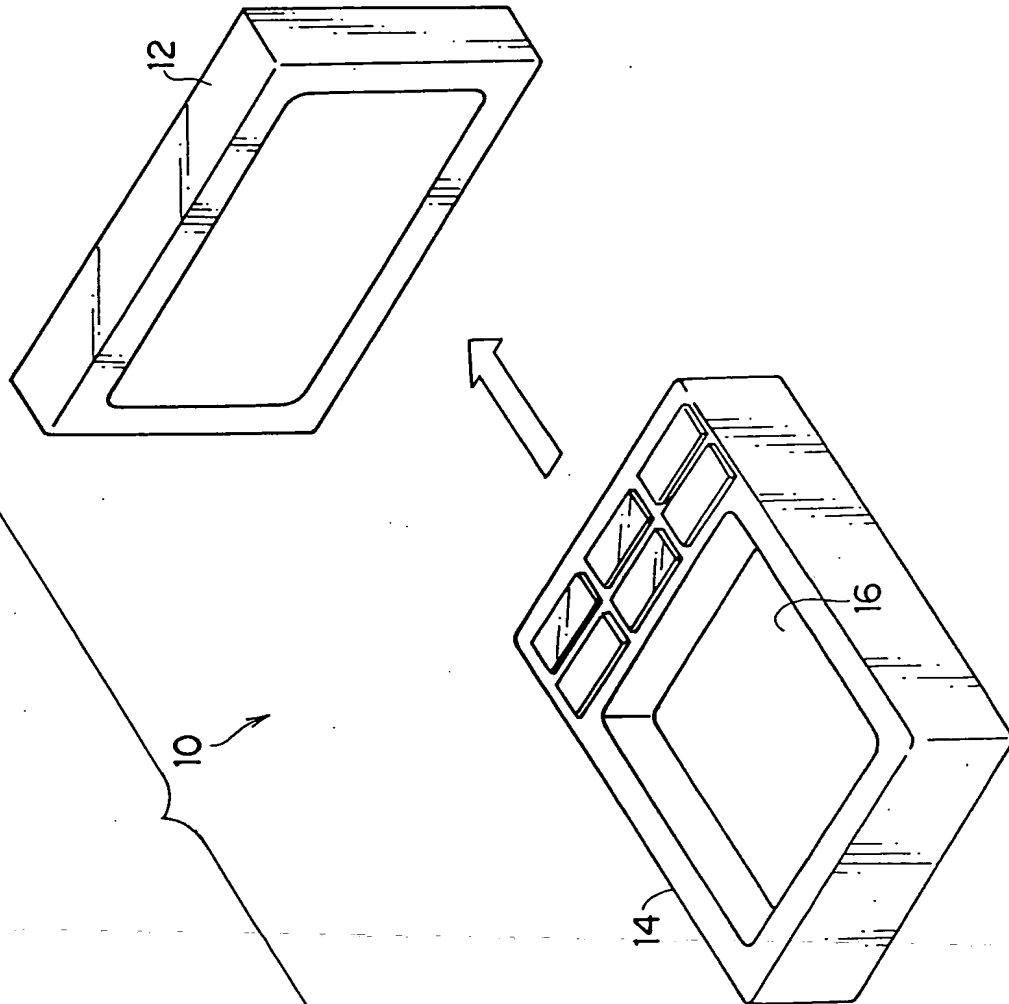


FIG. 2

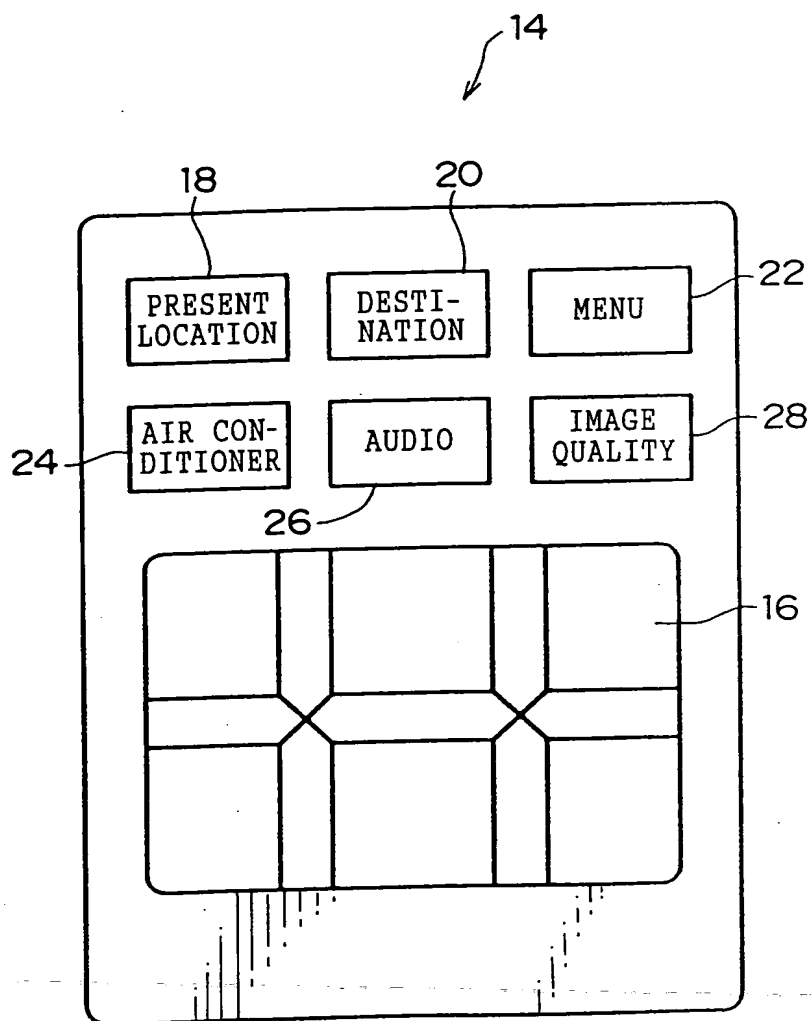


FIG. 3 is a schematic diagram of a circuit board 10 showing a grid of conductive traces 12 and 14. The grid is formed by a series of intersecting horizontal and vertical lines. The horizontal lines are labeled 12 and the vertical lines are labeled 14. The grid is divided into a central square region and four rectangular regions at the corners. The central square region is further divided into four smaller square regions by a horizontal and a vertical line. The four rectangular regions are also divided into smaller rectangular regions by horizontal and vertical lines. The grid is shown in a perspective view, with the lines converging towards the top of the page. The label 10 is located at the top left of the grid, and the labels 12 and 14 are located near the top and left edges of the grid, respectively.

FIG. 3

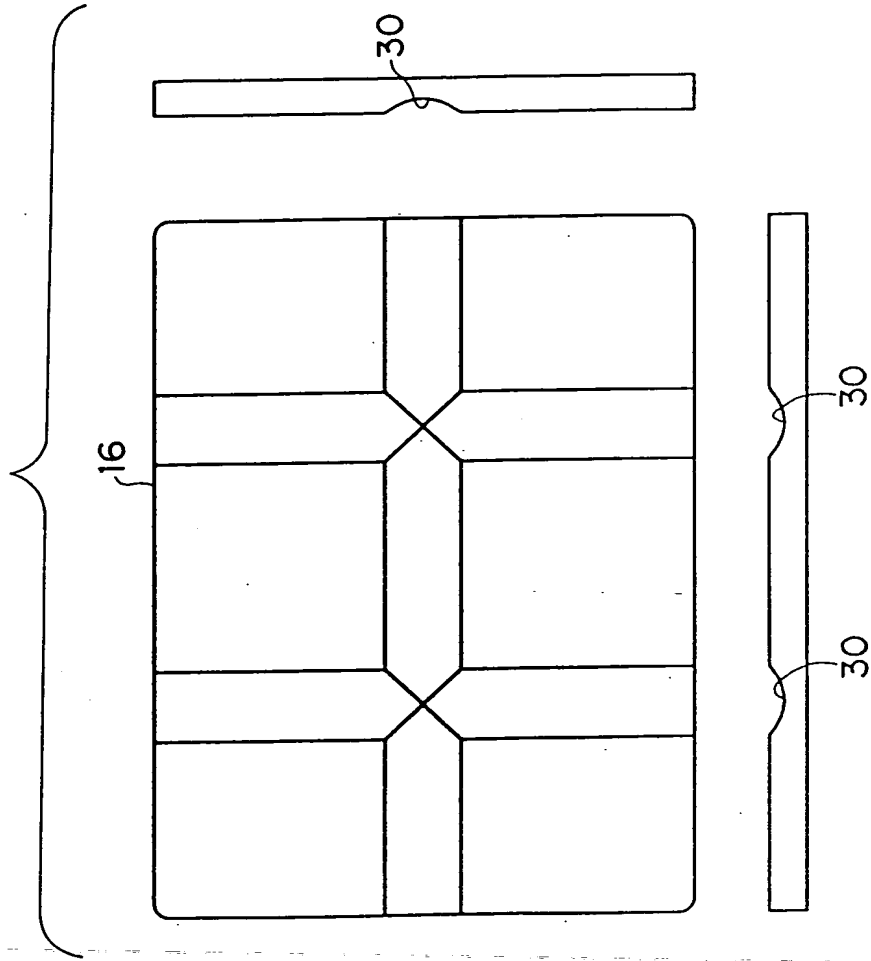


FIG. 4

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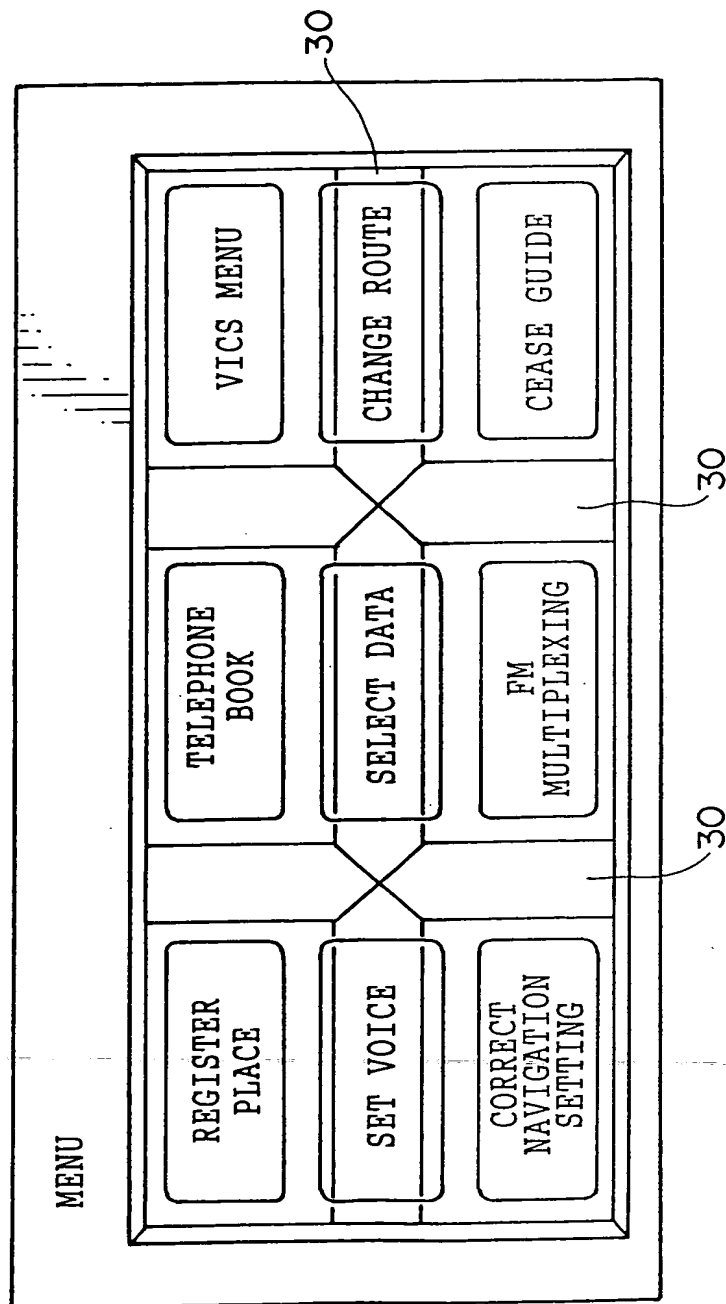
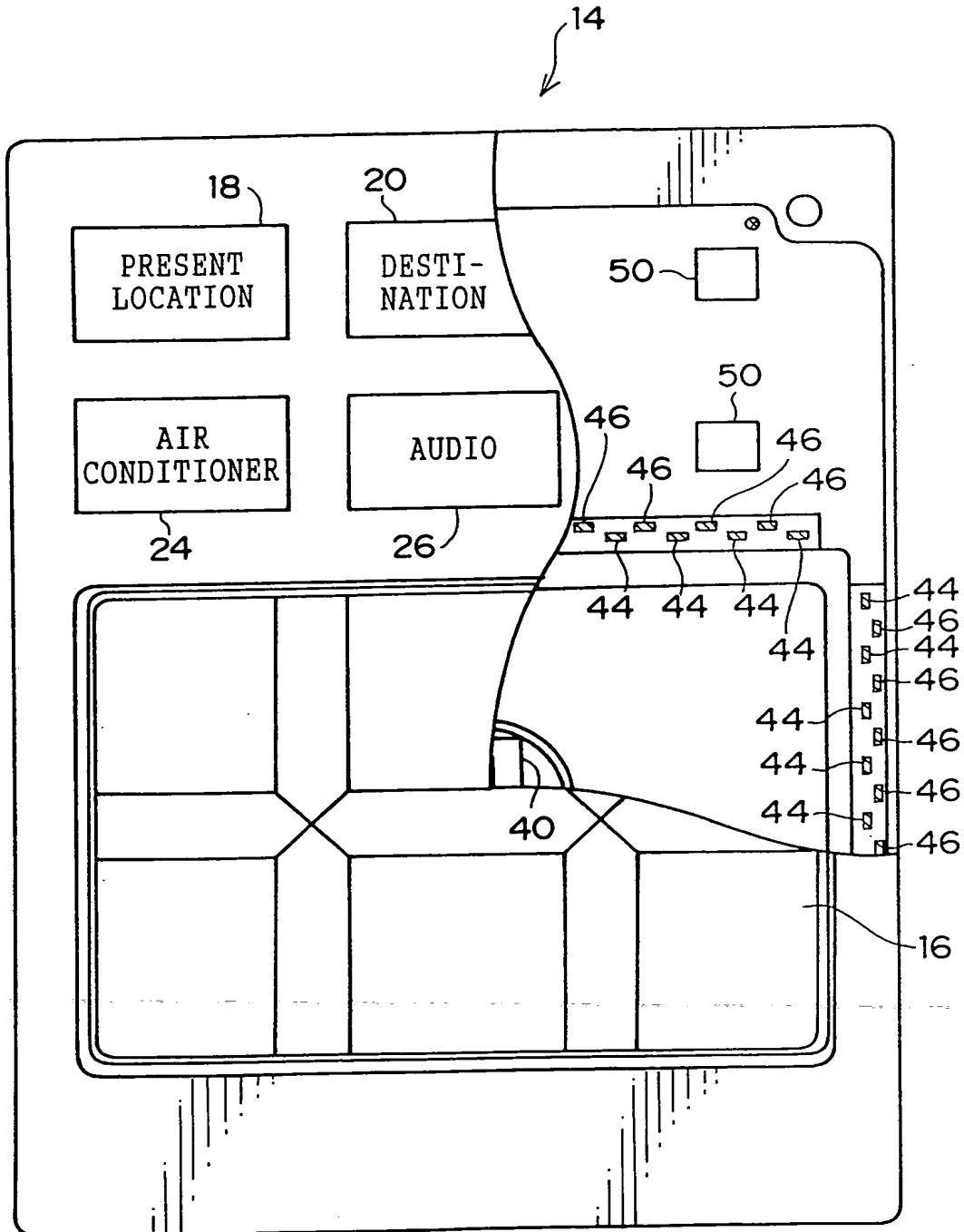
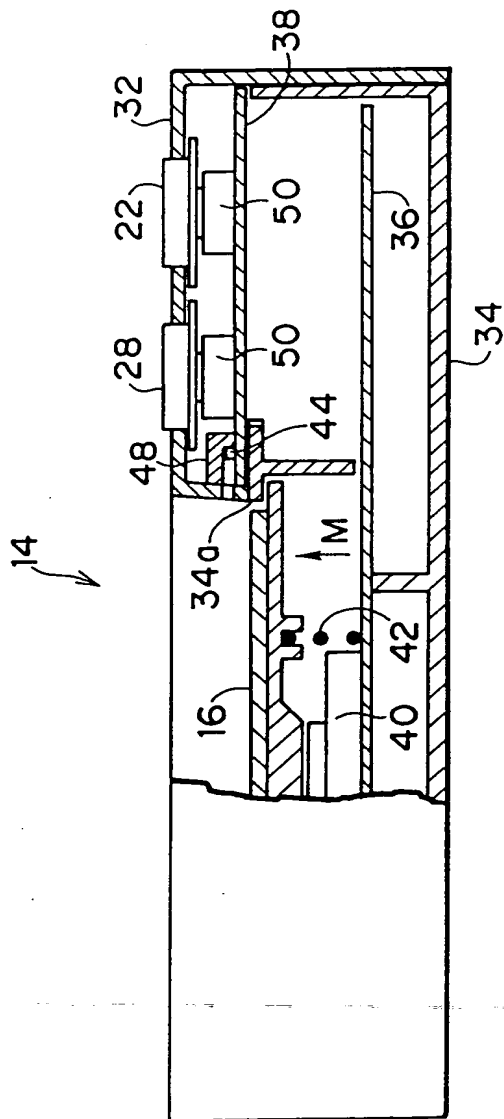


FIG. 5



F-1-G-6



# 15 ELEMENTS IN LATERAL DIRECTION

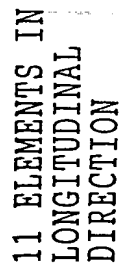


FIG. 8

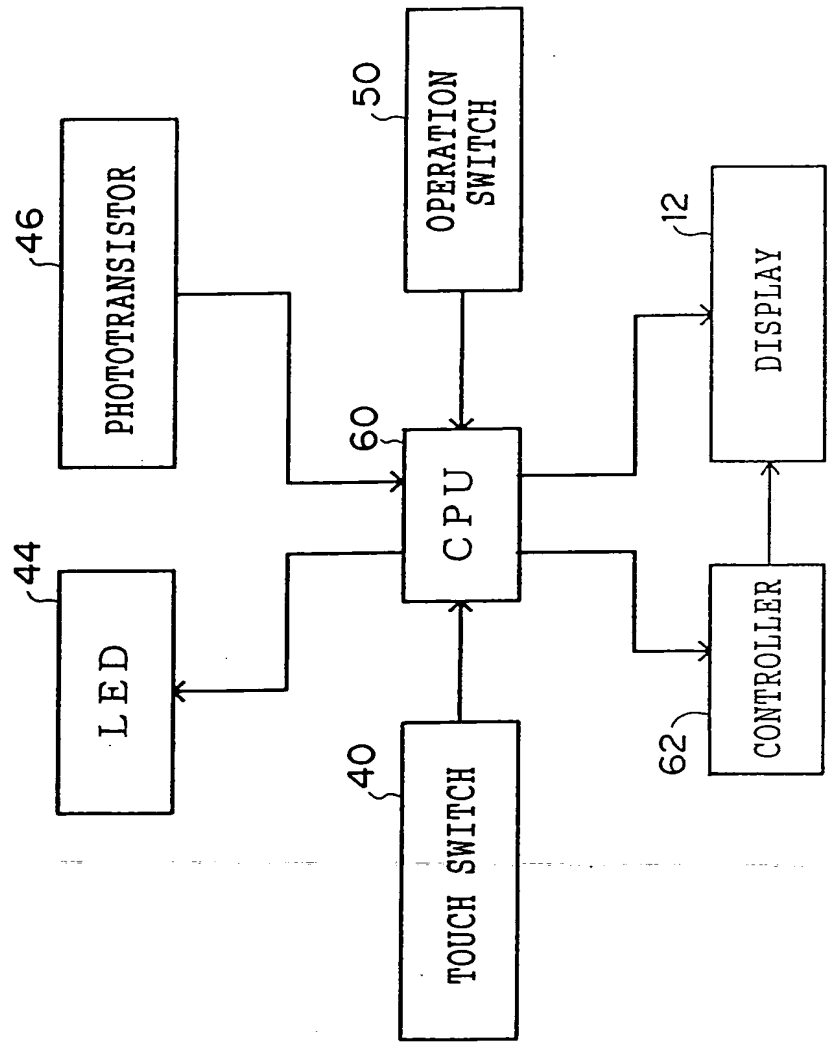




FIG. 9

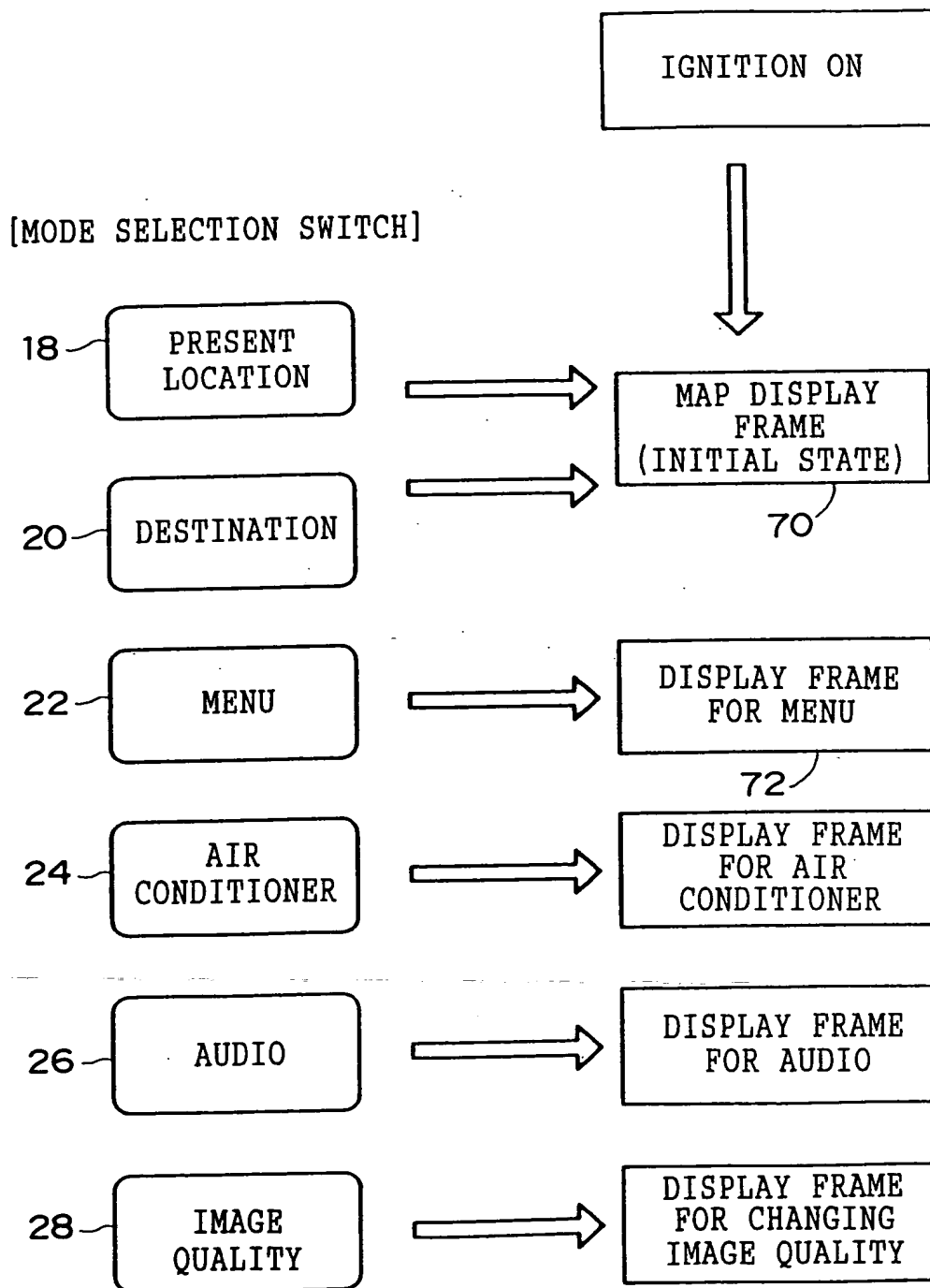
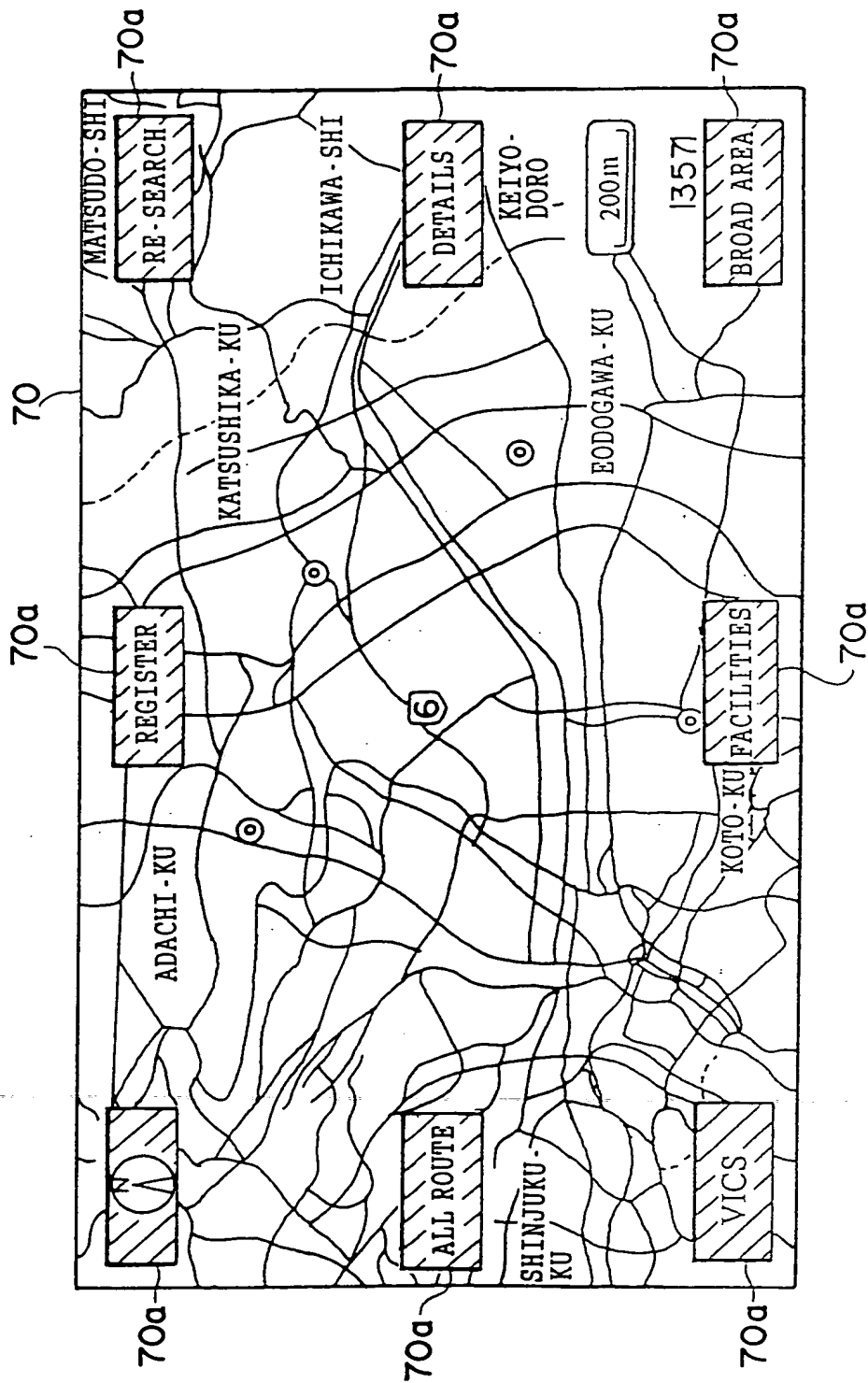


FIG. 10



F I G. 1 1 A

GOLF COURSE		GOLF COURSE OF AICHI PREFECTURE		
<input type="checkbox"/>	MIYOSHI CC	<div>▲</div> <div>▼</div> <div>RE-TURN</div>	<div>▲ ALPHA-BET</div> <div>▼ ALPHA-BET</div>	<div>▲ LINE</div> <div>▼ LINE</div> <div>DE-CIDE</div>
R	ROYAL CC			
A	AICHI CC			
<input type="checkbox"/>	AOI CC			
<input type="checkbox"/>	ASAHI CC			

F I G. 1 1 B

GOLF COURSE		GOLF COURSE OF AICHI PREFECTURE		
<input type="checkbox"/>	MIYOSHI CC	<div>▲ ALPHABET</div> <div>▼ ALPHABET</div>		
R	ROYAL CC			
A	AICHI CC			
		<div>▲</div> <div>▼</div> <div>RETURN</div>	<div>▲ LINE</div> <div>▼ LINE</div> <div>DECIDE</div>	

FIG. 12

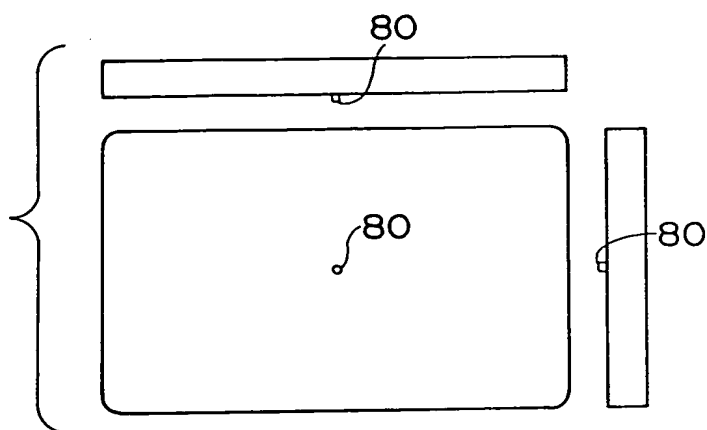


FIG. 13

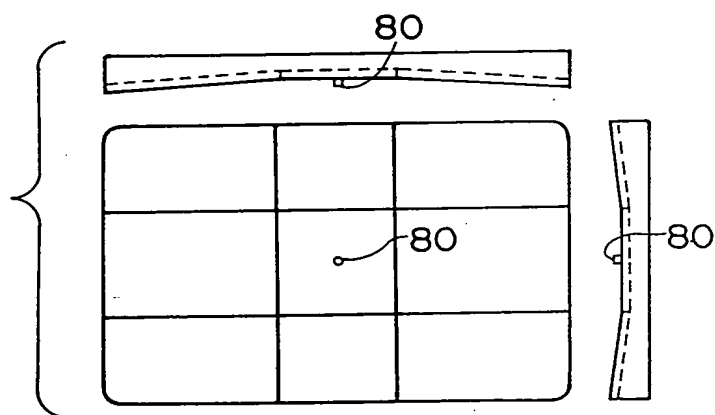


FIG. 14

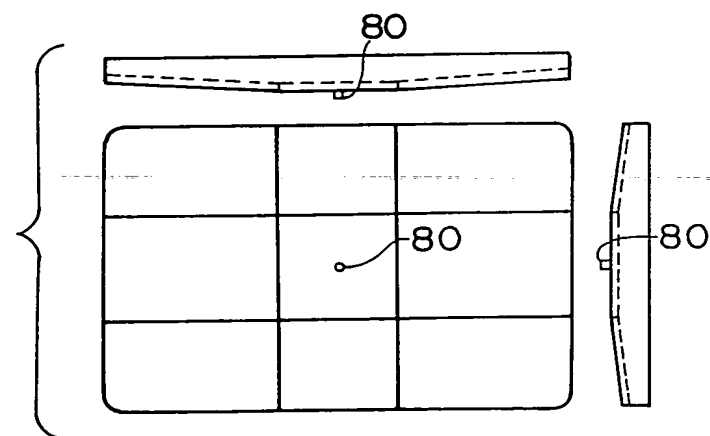


FIG. 15

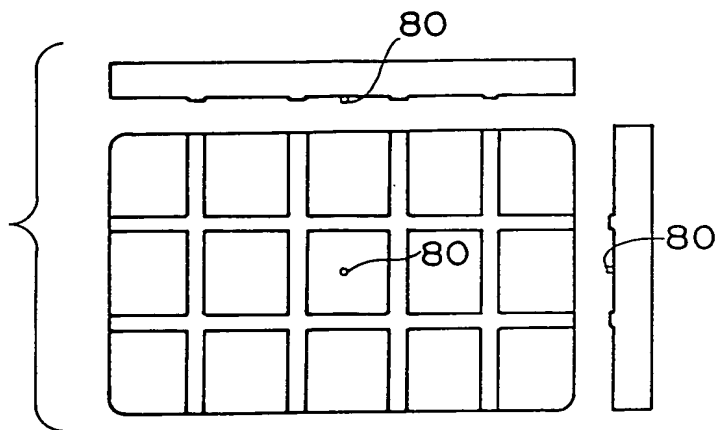


FIG. 16

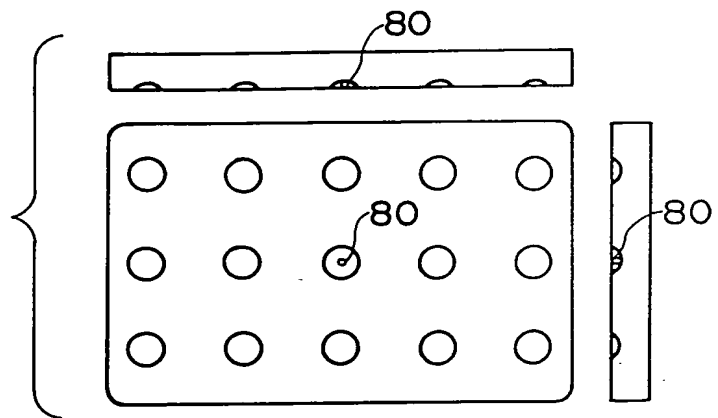


FIG. 17

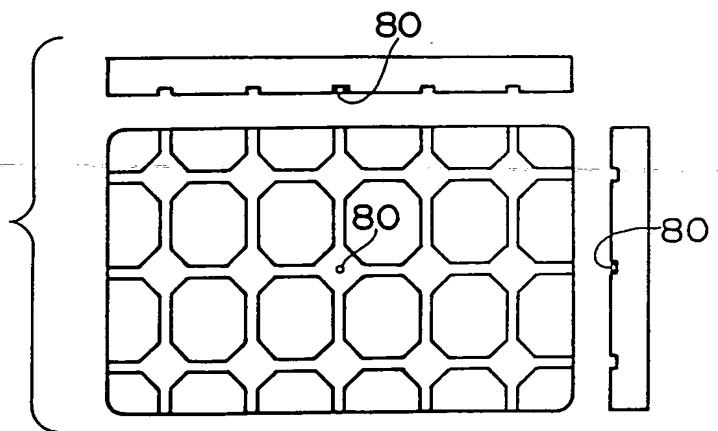


FIG. 18

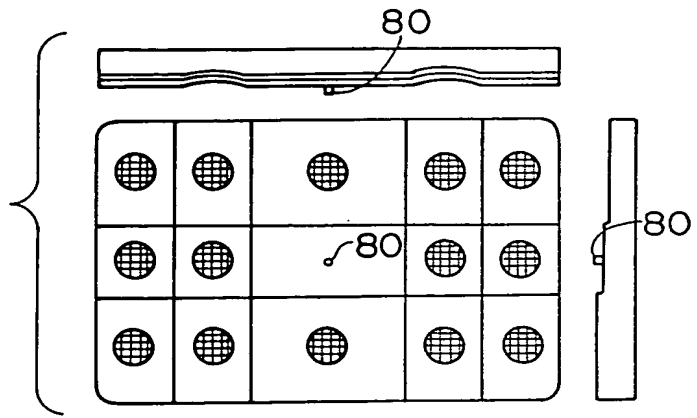


FIG. 19

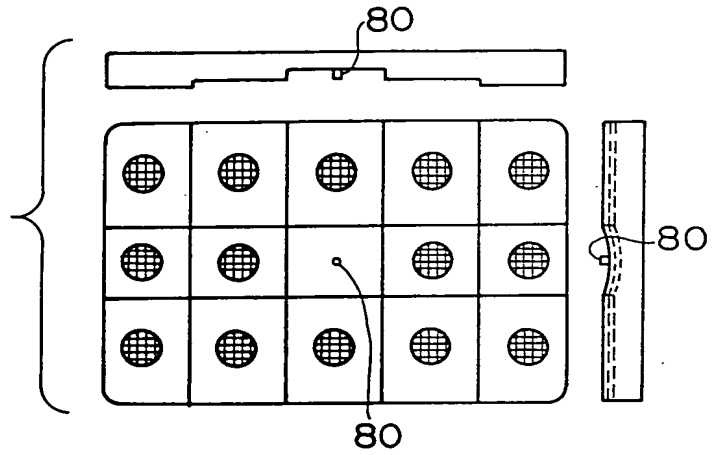


FIG. 20

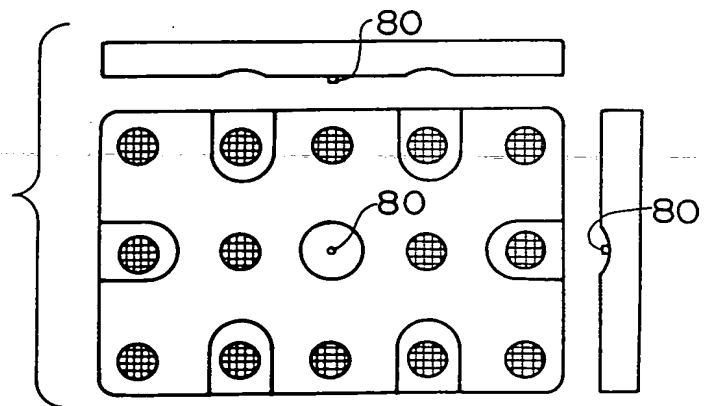


FIG. 21

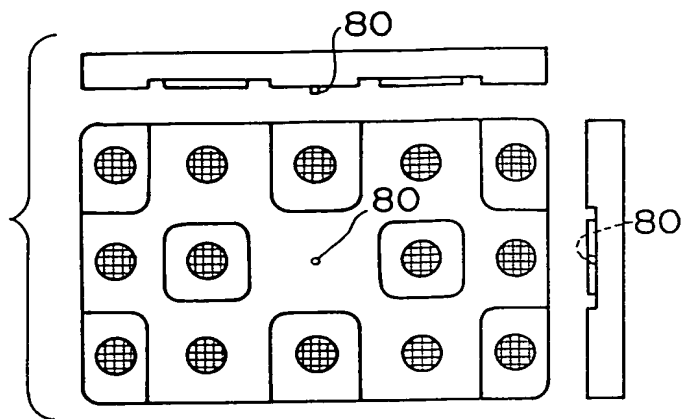


FIG. 22

